



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. 10/714,206 Confirmation No.: 2662  
Applicant : Madhu Avasarala, et al.  
Filed : November 13, 2003  
TC/AU: 2684  
Examiner : Unassigned  
Docket No.: 100628.53040US  
Customer No.: 23911  
Title: INTEGRATION OF DIVERSITY SWITCH IN COMBINATION WITH A T/R  
SWITCH FOR A RADIO TRANSCEIVER ON A SINGLE CHIP

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §§ 1.97 and 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The present Information Disclosure Statement is being filed (1) no later than three months from the application's filing date or (2) before the mailing date of the first Office Action on the merits (whichever is later), and therefore no certification under 37 C.F.R. §1.97(e) or fee under 37 C.F.R. §1.17(p) is required.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

If necessary, the paper should be considered as a petition for consideration of the Information Disclosure Statement under 37 C.F.R. §1.97(d)(2) and that the petition fee set forth in 37 C.F.R. §1.17(i) in accordance with 37 C.F.R. §1.97(d)(3) should be charged to Deposit Account No. 05-1323 (Docket 100628.53040US).

Respectfully submitted,

CROWELL & MORING LLP

Dated: April 14, 2004

By

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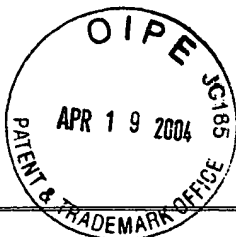
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**CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8A)**

*I hereby certify that this correspondence is, on the date shown below, being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.*

Date: 4-14-2004

Laura R. Dixon  
Laura R. Dixon



FORM PTO-1449 U.S. Department of Commerce Patent & Trademark Office  <b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>	Attorney Docket No. 100628.53040US	Serial No. 10/714,206
	Applicant: Madhu Avasarala, et al.	
	Filing Date November 13, 2003	Group 2684

U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date (if appropriate)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS								
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	TRANSLATION	
							Yes	No
	AK							
	AL							
	A M							
	AN							
	AO							
	AP							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
	AQ: LOW VOLTAGE, HIGH POWER T/R SWITCH MMIC USING LC RESONATORS, Tsuneo Tokumitso, et al., IEEE 1993 Microwave and Millimeter-Wave Monolithic Circuits Symposium, pp. 27-30.
	AR: AN INTEGRATED 5.2GHz CMOS T/R SWITCH WITH LC-TUNED SUBSTRATE BIAS, Niranjana Talwalkar, et al., ISSCC 2003/ Session 20/Wireless Local Area Networking/paper 20-7.